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7590

01/07/2004

Jack Q. Lever, Jr.
 McDERMOTT, WILL & EMERY
 600 Thirteenth Street, N.W.
 Washington, DC 20005-3096



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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/085,009 | 03/01/2002 | Masahiro Fukui | 60188-156 | 1135 |

TITLE OF INVENTION: WIRING METHOD IN LAYOUT DESIGN OF SEMICONDUCTOR INTEGRATED CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT AND FUNCTIONAL MACRO

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 04/07/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|------------------|----------|----------------|
| HOANG, QUOC DINH | 2818 | 438-129000 |

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. McDermott, Will & Emery
 2. _____
 3. _____

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. Osaka, JP

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Date)

Michael E. Fogarty, Reg. #36,139 3/4/2004

03/08/2004 DEMMANU2 00000155 500417 10085009

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